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The screenshot displays the Xilinx ISE Project Navigator interface for a project named 'MODEM\_YKB\_KART\_TEST'. The main window shows the 'Project Status' section with the following details:

Property	Value	Property	Value
Project File:	FTO_MODEM_YKB.xise	Parser Errors:	No Errors
Module Name:	MODEM_YKB_KART_TEST	Implementation State:	Synthesized
Target Device:	xc7k355t-1ffg901	Errors:	No Errors
Product Version:	ISE 14.6	Warnings:	4 Warnings (4 new)
		Routing Results:	
		Timing Constraints:	
		Final Timing Score:	

Below the status section is the 'Utilization Summary (estimated values)' table:

Category	Available	Utilization	Percentage
25	445200		0%
24	222600		0%
24	25		96%
27	300		9%
5	32		15%

The 'Detailed Reports' section shows:

Report Type	Errors	Warnings	Infos
08/27/2013	0	4 Warnings (4 new)	5 Infos (5 new)

The 'Secondary Reports' section shows 'Generated'.

The 'Date Generated: 08/27/2013 - 14:12:08'.

The 'Select IP' dialog box is open, showing a list of IP cores. The 'Search IP Catalog' field is empty. The 'All IP versions' checkbox is checked, and the 'Only IP compatible with chosen part' checkbox is unchecked.

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